

X-1376 US  
10/717,359

PATENT  
Conf. No.: 8181

IN THE UNITED STATES PATENT OFFICE



Applicant: Sudip K. Nag  
Assignee: Xilinx, Inc.  
Title: Method and Apparatus for Timing Characterization of Integrated Circuit Designs  
Serial No.: 10/717,359 File Date: 11/18/2003  
Examiner: Vuthe Siek Art Unit: 2825  
Docket No.: X-1376 US Conf. No.: 8181

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Mail Stop AMENDMENT  
COMMISSIONER FOR PATENTS  
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AMENDMENT IN RESPONSE TO FIRST OFFICE ACTION

Dear Sir:

In response to the First Office Action mailed from the Patent Office on August 8, 2005, please replace/substitute the following claims as indicated.

**Amendments to the claims** are reflected in the list of claims that begins on page 2 of this paper.

**Remarks** begin on page 6 of this paper.



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PTO/SB/21 (09-04)

Approved for use through 07/31/2006. OMB 0651-0031

U.S. Patent and Trademark Office; U.S. DEPARTMENT OF COMMERCE

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## TRANSMITTAL FORM

(to be used for all correspondence after initial filing)

Total Number of Pages in This Submission

Application Number 10/717,359

Filing Date November 18, 2003

First Named Inventor Sudip K. Nag

Art Unit 2825

Examiner Name Vuthe Sleik

Attorney Docket Number X-1376 US

### ENCLOSURES (Check all that apply)

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Remarks

### SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT

Firm Name XILINX, INC. CUSTOMER NO: 24309

Signature

Printed name Kim Kanzaki

Date November 2, 2005

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### CERTIFICATE OF TRANSMISSION/MAILING

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November 2, 2005

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